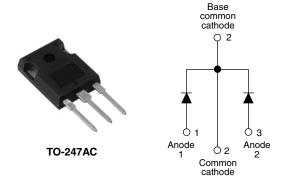
Vishay High Power Products

Schottky Rectifier, 2 x 40 A



PRODUCT SUMMARY				
I _{F(AV)}	2 x 40 A			
V _R	20 V			
I _{RM}	1100 mA at 125 °C			

FEATURES

- 150 °C T_J operation
- Center tap configuration
- Optimized for 3.3 V application
- Ultralow forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

This center tap Schottky rectifier has been optimized for ultralow forward voltage drop specifically for 3.3 V output power supplies. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	80	A		
V _{RRM}		20	V		
I _{FSM}	t _p = 5 μs sine	2200	A		
V _F	40 Apk, $T_J = 150 \ ^{\circ}C$ (per leg)	0.32	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	80CPQ020PbF	UNITS		
Maximum DC reverse voltage	V _R	20	V		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum averageper legforward currentper device			50 % duty cycle at $T_{\rm C}$ = 138 °C, rectangular waveform		40	
		I _{F(AV)}	50% utily cycle at $1_{\rm C} = 150\%$ C,	$T_{\rm C} = 136$ C, rectangular wavelonn		
Maximum peak one cycle non-repetitive surge current per leg		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated	2200	A
			10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	500	
Non-repetitive avalanche ene	ergy per leg	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 6 \text{ A}, L = 1.5 \text{ mH}$ 27		mJ	
Repetitive avalanche current	per leg	I _{AR}			А	

* Pb containing terminations are not RoHS compliant, exemptions may apply





80CPQ020PbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
) (_ (1)	40 A	T _J = 25 °C	0.46	V
		80 A		0.55	
Maximum forward		40 A T 105 %C	T 405.00	0.36	
voltage drop per leg	V _{FM} ⁽¹⁾	80 A T _J = 125 °C		0.46	
		40 A	- T _J = 150 °C	0.32	
		80 A		0.43	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	$T_J = 125 \ ^{\circ}C$ $V_R = 5 V$		110	
		$T_{\rm J} = 150 \ ^{\circ}{\rm C}$ $V_{\rm R} = 10 \ {\rm V}$		600	
		$T_J = 25 ^{\circ}C$	V _R = Rated V _R	5.5	mA
		$T_J = 125 \ ^{\circ}C$		1100	
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.185	V
Maximum junction capacitance per leg	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		6500	pF
Typical series inductance per leg	Ls	Measured lead to lead 5 mm from package body		7.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		10 000	V/µs

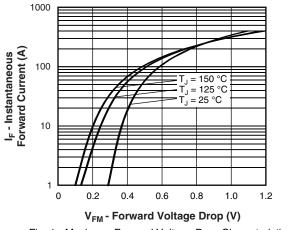
Note

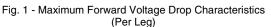
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

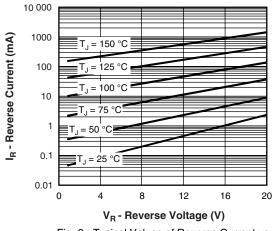
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and sto temperature range	orage	T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resista junction to case per leg	nce,	D		0.6	
Maximum thermal resista junction to case per packa	,	R _{thJC}	DC operation	0.3	°C/W
Typical thermal resistance case to heatsink	9,	R _{thCS}	Mounting surface, smooth and greased	0.25	
• • • • • •				6	g
Approximate weight				0.21	OZ.
Mounting torque	minimum			6 (5)	kgf ⋅ cm
	maximum			12 (10)	(lbf · in)
Marking device			Case style TO-247AC (JEDEC)	80CPQ020	

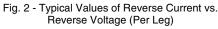


Schottky Rectifier, 2 x 40 A Vishay High Power Products









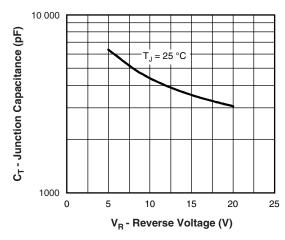


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

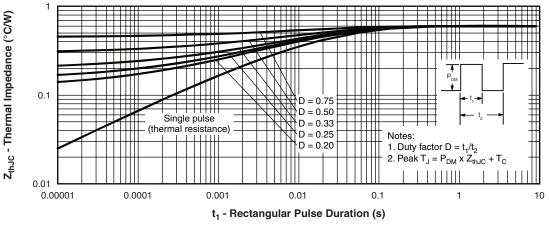
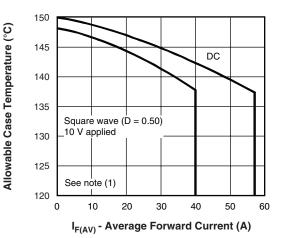


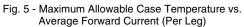
Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

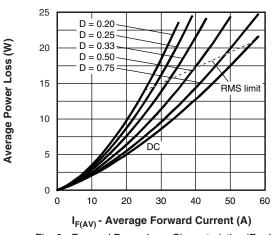
80CPQ020PbF

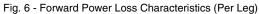
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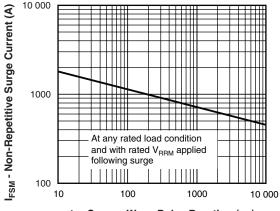
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t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

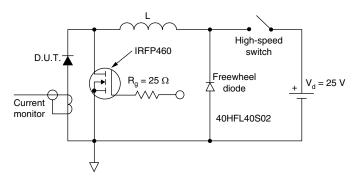


Fig. 8 - Unclamped Inductive Test Circuit

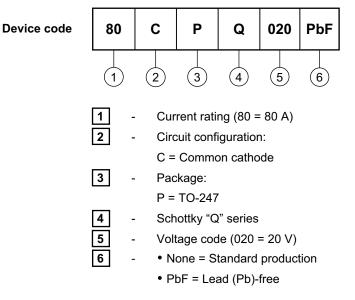
Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 D)$; $I_R at V_{R1} = 10 V$



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ORDERING INFORMATION TABLE



Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95223				
Part marking information	http://www.vishay.com/doc?95226			
SPICE model http://www.vishay.com/doc?95289				



Vishay

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